

INITIAL INFORMATION DATA SHEET

APPLICATION INFORMATION

Application Type:: Regular
Subject Matter:: Utility
Title:: Method and Apparatus for Testing
Circuitry Embedded Within a Field
Programmable Gate Array
Docket Number:: X-916 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: Twenty-Five
Small Entity?:: No
Petition included?:: No

INVENTOR INFORMATION

Inventor Authority Type:: Inventor
Primary Citizenship Ctry:: United Kingdom
Status:: Full Capacity
Given Name:: Nigel G.
Family Name:: Herron
Street:: 4628 Fallstone Court
City:: San Jose
State or Province:: CA
Postal or Zip Code:: 95124

Inventor Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Eric J.
Family Name:: Thorne
Street:: 901 Escalona Drive
City:: Santa Cruz
State or Province:: CA
Postal or Zip Code:: 95060

Inventor Authority Type:: Inventor
Primary Citizenship Ctry:: P. R. China
Status:: Full Capacity
Given Name:: Qingqi
Family Name:: Wang
Street:: 6052 Buffett Place
City:: San Jose
State or Province:: CA
Postal or Zip Code:: 95123

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

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|---|--------------|--|
| Representative Customer Number:: | 24309 | |
|---|--------------|--|

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124